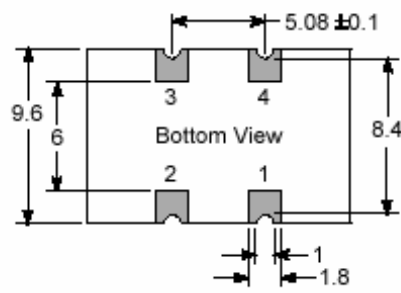
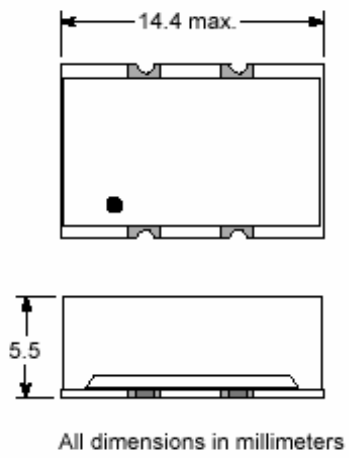




VCD2 PECL 4 PAD SMD

Package Option:	14.4 x 9.6 x 5.5 mm 4 pad SMD
Frequency Range:	19.440 to 212.5 MHz
Supply Voltage:	P = + 5.0 Vdc \pm 5 % PECL Q = + 3.3 Vdc \pm 5% PECL
Electrical Options:	7 = Tri-state 8 = Complementary 9 = N/ C
Stability Options:	A = \pm 100 PPM 0C to +70C B = \pm 50 PPM 0C to +70C E = \pm 25 PPM 0C to +70C
Calibration Tolerance:	\pm 25 PPM at +25 \pm 2C
Output Level "1": "0":	V _{cc} - 1.025 V Minimum V _{cc} - 1.620 V Maximum
Duty Cycle:	45/ 55 %
Rise time/Fall time:	2 ns Maximum (20 % to 80% of waveform)
Jitter:	3.5 ps RMS Maximum
Start-Up:	10 ms Maximum
Aging:	< 5.0 PPM/First year at +40C dynamic
Load:	50 ohms into V _{cc} - 2V
Current:	100 mA Maximum
Storage Temperature:	-55°C to +125°C
STD Package:	Anti Static Tubes



- Pin 1: Complimentary Output
- Pin 2: Ground
- Pin 3: Output
- Pin 4: Vcc

