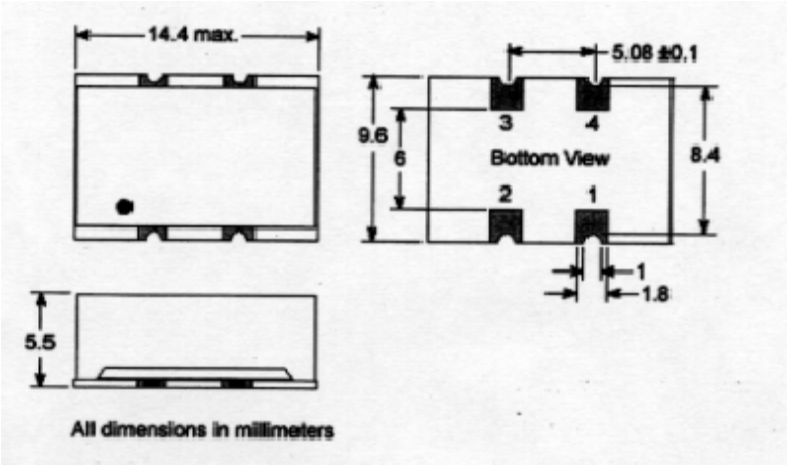
VVD2

HCMOS 9.6 x 14.4 x 4.5 VCXO

Package:	14.5 mm x 9.6 mm x 5.5 mm 4 Pin SMD
Frequency Range:	1.0 MHz to 160.0 MHz.
Electrical Options:	**A** = + 5.0 Vdc ± 20 % Linearity **B** = + 3.3 Vdc ± 20 % Linearity **E** = + 5.0 Vdc ± 10 % Linearity **F** = + 3.3 Vdc ± 10 % Linearity **G** = + 5.0 Vdc ± 5 % Linearity
Pull Range (+ Slope): (0.3 to 3.0 V for Vcc=3.3V) (0.5 to 5.0 V for Vcc=5.0V)	**1** = ± 50 PPM Minimum **2** = ± 100 PPM Minimum **3** = ± 150 PPM Minimum **4** = ± 200 PPM Minimum
Stability Options:	**A** = ± 100 PPM 0°C to + 70°C **B** = ± 50 PPM 0°C to + 70°C **C** = ± 100 PPM -40°C to +85°C **D** = ± 50 PPM -40°C to +85°C **E** = ± 25 PPM 0°C to + 70°C
Calibration Tolerance:	± 25 PPM at 25°C±2°C at Center Voltage
Duty Cycle:	40/60
Start-Up:	20 ms Maximum
Aging:	< 5 PPM/ 1st year at +40°C dynamic
Load:	15 pF HCMOS or 10 TTL Minimum
Current:	20 mA Maximum ≤ 20 MHz 40 mA Maximum ≤ 30 MHz 50 mA Maximum > 30 MHz



PINOUTS: Pin1=Vcont, Pin2=GND, Pin3=Output, Pin4=Vdd